

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	798	programmable adj interrupt adj controller\$1	USPAT	OR	OFF	2006/09/25 13:16
S2	4	programmable adj interrupt adj controller\$1 same partition\$2	USPAT	OR	OFF	2006/09/25 13:16
S3	4	programmable adj interrupt adj controller\$1 same partition\$2	US-PGPUB	OR	OFF	2006/09/25 13:17
S4	0	programmable adj interrupt adj controller\$1 same partition\$2	EPO; JPO; IBM_TDB	OR	OFF	2006/09/25 13:19
S5	0	programmable adj interrupt adj controller\$1 same domain\$1	USPAT	OR	OFF	2006/09/25 13:20
S6	0	programmable adj interrupt adj controller\$1 same domain\$1	US-PGPUB	OR	OFF	2006/09/25 13:20
S7	0	programmable adj interrupt adj controller\$1 same domain\$1	EPO; JPO; IBM_TDB	OR	OFF	2006/09/25 13:20
S8	74	programmable adj interrupt adj controller\$1.clm. and g06f\$.ipc.	USPAT	OR	OFF	2006/09/25 13:23
S9	26	programmable adj interrupt adj controller\$1.clm. same processor\$1 and g06f\$.ipc.	USPAT	OR	OFF	2006/09/25 13:23
S10	8	programmable adj interrupt adj controller\$1.clm. same processor\$1 and g06f\$.ipc.	US-PGPUB	OR	OFF	2006/09/25 13:33
S11	0	programmable adj interrupt adj controller same processor\$1interrupting	USPAT	OR	OFF	2006/09/25 13:36
S12	0	programmable adj interrupt adj controller same processor adj interrupting	USPAT	OR	OFF	2006/09/25 13:36
S13	0	interrupts with unpartition\$2 with partition\$2	USPAT	OR	OFF	2006/09/25 13:49
S14	0	interrupts with unpartition\$2	USPAT	OR	OFF	2006/09/25 13:49
S15	137	interrupts with partition\$2	USPAT	OR	OFF	2006/09/25 13:49
S16	2	interrupts with partition\$2 same interrupt adj controller	USPAT	OR	OFF	2006/09/25 13:49
S17	3	interrupts with partition\$2 same interrupt adj controller	US-PGPUB	OR	OFF	2006/09/25 13:50
S18	0	interrupts with partition\$2 same interrupt adj controller	EPO; JPO; IBM_TDB	OR	OFF	2006/09/25 13:50
S19	3	interrupt adj controller adj subsystem	USPAT	OR	OFF	2006/09/25 14:04
S20	0	interrupt adj controller adj subsystem	US-PGPUB	OR	OFF	2006/09/25 14:06
S21	0	interrupt adj controller adj subsystem	EPO; JPO; IBM_TDB	OR	OFF	2006/09/25 14:06

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S22	45	first adj processor adj interrupt\$3	USPAT	OR	OFF	2006/09/25 14:16
S23	3	first adj processor adj interrupt\$3 same second adj processor adj interrupt\$3	USPAT	OR	OFF	2006/09/25 14:12
S24	4	first adj processor adj interrupt\$3 same second adj processor adj interrupt\$3	US-PGPUB	OR	OFF	2006/09/25 14:14
S25	0	first adj processor adj interrupt\$3 same second adj processor adj interrupt\$3	EPO; JPO; IBM_TDB	OR	OFF	2006/09/25 14:14
S26	1	first adj processor adj interrupt\$3 and third adj processor adj interrupt\$3	USPAT	OR	OFF	2006/09/25 14:15
S27	0	first adj processor adj interrupt\$3 and third adj processor adj interrupt\$3	US-PGPUB	OR	OFF	2006/09/25 14:16
S28	0	first adj processor adj interrupt\$3 and third adj processor adj interrupt\$3	EPO; JPO; IBM_TDB	OR	OFF	2006/09/25 14:16
S29	1	first adj processor adj interrupt\$3	EPO; JPO; IBM_TDB	OR	OFF	2006/09/25 14:22
S30	2	initializing adj interrupt adj controller	USPAT	OR	OFF	2006/09/25 14:29
S31	4	initializing adj interrupt adj controller	US-PGPUB	OR	OFF	2006/09/25 14:30
S32	0	initializing adj interrupt adj controller	EPO; JPO; IBM_TDB	OR	OFF	2006/09/25 14:30
S33	23	taylor.in. with billy	USPAT	OR	OFF	2006/09/25 14:33
S34	12	kumar.in. with mohan	USPAT	OR	OFF	2006/09/25 14:34
S35	2	smoak.in. with wilson	USPAT	OR	OFF	2006/09/25 14:35
S36	9	\$2shea.in. with david	USPAT	OR	OFF	2006/09/25 14:36
S37	0	coury.in. with bassam	USPAT	OR	OFF	2006/09/25 14:37
S38	80	coury.in.	USPAT	OR	OFF	2006/09/25 14:37
S39	0	coury.in. and intel.as.	USPAT	OR	OFF	2006/09/25 14:37
S40	0	lam.in. with priscilla	USPAT	OR	OFF	2006/09/25 14:38
S41	23	lam.in. and intel.as.	USPAT	OR	OFF	2006/09/25 14:38
S42	0	slaight.in. with tom	USPAT	OR	OFF	2006/09/25 14:38
S43	5	slaight.in. and intel.as.	USPAT	OR	OFF	2006/09/25 14:39
S44	259	710/266.ccls.	USPAT	OR	OFF	2006/09/25 14:47
S45	0	710/266.ccls. and programmable adj interrupt adj controller\$1 same partition\$2	USPAT	OR	OFF	2006/09/25 14:59
S46	0	710/266.ccls. and interrupts with partition\$2 same interrupt adj controller	USPAT	OR	OFF	2006/09/25 15:00

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S47	0	710/266.ccls. and programmable adj interrupt adj controller\$1 same domain\$1	USPAT	OR	OFF	2006/09/25 15:01
S48	0	710/266.ccls. and interrupt adj controller adj subsystem	USPAT	OR	OFF	2006/09/25 15:01
S49	16	710/266.ccls. and programmable adj interrupt adj controller\$1.clm. same processor\$1 and g06f\$.ipc.	USPAT	OR	OFF	2006/09/25 15:03
S50	876	710/260.ccls.	USPAT	OR	OFF	2006/09/25 15:12
S51	0	710/260.ccls. and interrupt adj controller adj subsystem	USPAT	OR	OFF	2006/09/25 15:21
S52	1	710/260.ccls. and programmable adj interrupt adj controller\$1 same partition\$2	USPAT	OR	OFF	2006/09/25 15:22
S53	0	710/260.ccls. and programmable adj interrupt adj controller\$1 same domain\$1	USPAT	OR	OFF	2006/09/25 15:22
S54	7	710/260.ccls. and programmable adj interrupt adj controller\$1.clm. same processor\$1 and g06f\$.ipc.	USPAT	OR	OFF	2006/09/25 15:22
S55	1	710/260.ccls. and interrupts with partition\$2 same interrupt adj controller	USPAT	OR	OFF	2006/09/25 15:24
S56	130	712/13.ccls.	USPAT	OR	OFF	2006/09/25 15:24
S57	0	712/13.ccls. and interrupt adj controller adj subsystem	USPAT	OR	OFF	2006/09/25 15:28
S58	0	712/13.ccls. and interrupts with partition\$2 same interrupt adj controller	USPAT	OR	OFF	2006/09/25 15:28
S59	0	712/13.ccls. and programmable adj interrupt adj controller\$1 same partition\$2	USPAT	OR	OFF	2006/09/25 15:29
S60	0	712/13.ccls. and programmable adj interrupt adj controller\$1 same domain\$1	USPAT	OR	OFF	2006/09/25 15:29
S61	0	712/13.ccls. and programmable adj interrupt adj controller\$1.clm. same processor\$1 and g06f\$.ipc.	USPAT	OR	OFF	2006/09/25 15:29